

# Digital System Design Automation Using VHDL

## Sample Course Syllabus - Placeholder PDF

This is a temporary placeholder file created only to test website links on GitHub Pages. Replace it later with the official course syllabus PDF.

Field	Placeholder content
Instructor	Ahmad Shabani
Institution	To be replaced with official syllabus details
Course format	Lectures, exercises, assignments, and project-based learning
Topics	Foundations, implementation concepts, practical examples, and assessment
Status	Test file for website link validation

## Website test note

If this PDF opens from the Teaching page, the corresponding syllabus link is working correctly.

Replace this file with your final official syllabus while keeping the same filename, or update the link in teaching.html.